	Application No.	
	Application No.	Applicant(s)
	09/900,946	HIRAHARA ET AL.
Notice of Allowability	Examiner	Art Unit
	James M. Mitchell	2827
The MAILING DATE of this communication app ars on th cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiativ of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>3/12/2004</u> .		
2. The allowed claim(s) is/are <u>1-19</u> .		
3. The drawings filed on 10 July 2001 are accepted by the Examiner.		
 4. Acknowledgment is made of a claim for foreign priority una) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONMITHIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give 1. CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftsperson 1) hereto or 2) To Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1.4 each sheet. Replacement sheet(s) should be labeled as such in the such as the sheet. Replacement sheet(s) should be labeled as such in the such as the supplication number (see 37 CFR 1.5 each sheet. Replacement sheet(s) should be labeled as such in the such as the such as the supplication number (see 37 CFR 1.5 each sheet. Replacement sheet(s) should be labeled as such in the such as the such	been received. been received in Application No cuments have been received in this in of this communication to file a reply of ENT of this application. Itted. Note the attached EXAMINER's reason(s) why the oath or declarate to be submitted. It be submitted. It is now a property of the comment of the	national stage application from the complying with the requirements S AMENDMENT or NOTICE OF tion is deficient. 948) attached office action of the back) of
7. DEPOSIT OF and/or INFORMATION about the depose attached Examiner's comment regarding REQUIREMENT F		
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08 Paper No./Mail Date	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☑ Examiner's Amendn	e

DETAILED ACTION

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Katherine Pauley on June 10, 2004.

The application has been amended in order to overcome the prior art as follows:

Claims

In claim 1, line 3 after "chip" insert - - surrounded by insulating material- -,

In claim 1, line 8 delete "by soldering or pressure welding" and insert - - such that the first and second main surfaces are formed on the predetermined two power terminals--;

In claim 4, Line 4 delete "the second main surface" and in line 5 delete "one of the predetermined;

In claim 4, Line 4 after "connected to" insert - - another power terminal of said- - and in line 5 after "power terminals" insert - - by soldering or pressure welding" and in Line 1 after "wherein" insert - - one face of said at least one semiconductor chip interposed between said two power terminals is connected to one power terminal of said two power terminals by soldering or pressure welding, and another face - -;

In claim 11, line 3 after "chip" insert - - surrounded by insulating material- -,
In claim 11, line 12 after "pressure welding" insert - - such that the first and second main surfaces are formed on the predetermined two power terminals--.

Allowable Subject Matter

The following is an examiner's statement of reasons for allowance: the prior art does not disclose or make obvious three power terminals provided one above the other with a chip surrounded by insulating material sandwiched between and electrically connected to two power terminals wherein one terminal is parallel with a first main surface of the chip and the other terminal is parallel to a second main surface that is opposite and parallel the first main surface including all the limitations of the independent claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to James M. Mitchell whose telephone number is (571) 272-1931. The examiner can normally be reached on M-F 10:30-8:00.

Application/Control Number: 09/900,946 Page 4

Art Unit: 2827

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on (571) 272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

June 10, 2004

KAMAND CUNEO SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800